

AMENDMENTS TO THE CLAIMS

Please amend the claims as follows:

1. (Original) An image signal processor for performing image processing on a first image signal representative of an image of a subject field captured by an imaging device to produce a second image signal, comprising:

a first memory for storing therein the first image signal;

a plurality of image processors for each performing image processing, different from each other, on the stored first image signal to produce the second image signal different from each other; and

a second memory for storing therein the second image signals produced,

wherein said plurality of image processors include types and parameters of the image processing such that at least one of the types and parameters of the image processing are different between said plurality of image processors.

2. (Original) The image signal processor according to claim 1 wherein the types of image processing performed by said plurality of image processors include at least one selected from a group consisting of a change of brightness, a change of gradation change characteristics, a correction of a color temperature, a change of saturation, a change of a contour, a change of a compression ratio and a change of a black level of the first image signal stored in said first memory,

the parameters of image processing being of the at least one selected from the group,

said plurality of image processors performing the image processing of the at least one selected from the group on the first image signal stored in said first memory according to the parameters of image processing.

3. (Original) The image signal processor according to claim 1 wherein each of said plurality of image processors corresponds to one of a plurality of display units which are provided for visualizing the image represented by the second image signals stored in said second memory,

each of said plurality of image processors processing, according to the parameters, the second image signals to be displayed on one of the display units which corresponds to said image processor.

4. (Original) The image signal processor according to claim 3 wherein the plurality of display units include a CRT (Cathode Ray Tube) display and an image printer.

5. (Original) The image signal processor according to claim 2, further comprising a divider circuit for dividing the first image signal stored in said first memory into a highlight area and a shadow area,

each of said plurality of image processors performing the image processing in which at least one of the types and the parameters of the image processing differs between the highlight area and the shadow area.

6. (Original) The image signal processor according to claim 1 wherein said second memory is detachably connected to said image signal processor.

7. (Original) An image signal processor for performing image processing on a first image signal representative of an image of a subject field captured by an imaging device to produce a second image signal, comprising:

a first memory for storing therein the first image signal;

a plurality of image processors for each performing image processing, different from each other, on the stored first image signal to produce a third image signal different from each other;

a second memory for storing therein the third image signals produced, and

an image composer circuit for composing the third image signals to produce the second image signal,

wherein said plurality of image processors include types and parameters of the image processing such that at least one of the types and parameters of the image processing are different between said plurality of image processors.

8. (Original) The image signal processor according to claim 7, further comprising a divider circuit for dividing the first image signal stored in said first memory into a highlight area and a shadow area,

wherein said plurality of image processors are provided correspondingly to the highlight area and the shadow area, each of said plurality of image processors performing the image processing for one of the highlight area and the shadow area to produce the third image signals.

9. (Original) A method of processing a first image signal representative of an image of a subject field captured by an imaging device to produce a second image signal, comprising the steps of:

storing the first image signal in a memory;

performing a same type of image processing on the stored first image signal according to parameters of image processing different from each other to produce the second image signals; and

storing the produced second image signals in a memory.

10. (Original) Imaging apparatus comprising:

an imaging device for capturing an image of a subject field and producing a first image signal representative of the subject field;

a first memory for storing therein the first image signal;

a plurality of image processors for each performing image processing, different from each other, on the stored first image signal to produce the second image signal different from each other;

a second memory for storing therein the second image signals produced;

and

an image composer circuit for composing the second image signals to produce a third image signal,

said plurality of image processors including types and parameters of the image processing such that at least one of the types and parameters of the image processing are different between said plurality of image processors,

the types of image processing including at least one selected from a group consisting of a change of brightness, a change of gradation change characteristics, a correction of a color temperature, a change of saturation, a change of a contour, a change of a compression ratio and a change of a black level of the first image signal stored in said first memory,

the parameters of image processing being of the at least one selected from the group,

whereby said plurality of image processors perform the image processing of the at least one selected from the group on the first image signal stored in said first memory according to the parameters of image processing.

11. (New) The image signal processor according to claim 1, wherein each of said plurality of image processors directly receives the stored first image signal as input.

12. (New) The method of processing according to claim 9, wherein each of said plurality of image processing directly receives the stored first image signal as input.